

INTEGRATED CIRCUIT DEVICES HAVING DELAY CIRCUITS FOR
CONTROLLING SETUP/DELAY TIMES OF DATA SIGNALS THAT ARE
PROVIDED TO MEMORY DEVICES AND METHODS OF OPERATING SAME

Abstract of the Disclosure

An integrated circuit device includes a delay circuit that is configured to delay a clock signal and is further configured to generate an output data signal in response to the delayed clock signal and an input data signal. Multiple devices are configured to respectively receive the output data signal in response to the clock signal.

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